

Docket No. 740756-2205

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ritsuko KAWASAKI et al.

Serial No. 09/651,889

Filed: August 30, 2000

For: SEMICONDUCTOR DEVICE,
MANUFACTURING METHOD
THEREOF AND ELECTRONIC
DEVICE

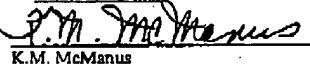
-) RESPONSE UNDER 37 CFR 1.116
-) EXPEDITED PROCEDURE
-) EXAMINING GROUP 2814
-) Examiner: D. Farahani
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CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office: Fax No. (703) 872-9319 on February 4, 2003.


 K.M. McManus

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Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

In response to the Examiner's final Office Action dated November 4, 2002, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

 Please amend claims 1-3, and 11-16 as follows:

1. (Thrice Amended) A semiconductor device comprising a TFT containing an active layer having a channel forming region,
- wherein a portion of the channel forming region is convexed or concaved in a channel width direction.